

Low-Power Temperature Sensor with Complementary-Metal-Oxide-Semiconductor Circuits

Shu-Chung Yi*

Department of Computer Science and Information Engineering, National Changhua University of Education, Changhua City, 500 Taiwan, R.O.C.

(Received November 5, 2012; accepted February 4, 2013)

Key words: CMOS sensor, PTAT, subthreshold region, temperature sensor

A low-power smart temperature sensor is proposed in this paper. The sensor was composed of a proportional-to-absolute-temperature (PTAT) circuit and a ring oscillator. The PTAT circuit was used to generate a temperature-related current. The current was used to drive the ring oscillator, which may generate a temperature-related oscillated signal. The sensor was implemented by the 0.35 μm 2P4M TSMC complementary-metal-oxide-semiconductor (CMOS) process technology. The power consumption is about 195.15 nW. The linearity of the output frequency versus temperature is marked by the *R*-square rule. The value of the linearity is 0.98 in the entire temperature range. The proposed sensor requires only one supply voltage of 3.3 V. The core area of the sensor is small enough for combining itself with other circuits that require temperature monitoring.

1. Introduction

In the near future, information network systems with diverse smart sensors will be developed and distributed all over the world to make infrastructures for the information age. Such network systems require a great number of sensors that measure several physical parameters in the environment.^(1–3)

These sensors must operate with ultralow power because they will likely be arranged under conditions where they will have to obtain essential energy from poor energy sources, such as microbatteries and solar cells.⁽⁴⁾

Traditional sensors have the ability to sense environment parameters, but they do not have signal processing and control functions. The instrument industry did not propose the concept of a smart sensor until the seventies and it devised a way to put the process and control of the sense signal into a single sensor element.

The sensor was called smart sensor in America and intelligent sensor in Europe. The smart sensor is constructed with the logic function, control function, and perceive

*Corresponding author: e-mail: scyi@cc.ncue.edu.tw

element in the same chip using integration technology, so the smart sensor is better than the conventional sensor, whether in the measurement or diverse function of the smart sensor.⁽⁵⁾

Temperature sensors have become common elements in a broad range of recent integrated circuits. They are used for measurements of temperature and indirectly for power management of physical, biomedical, and industrial applications.

In the smart temperature sensor, the voltage or current source was applied into the analog-to-digital converter (ADC). The output of the temperature sensor generated a digital output, such as an oscillated square wave. Thus, the conventional smart temperature sensor can be implemented by using the temperature sensor and ADC. The block diagram of the conventional smart temperature sensor⁽⁶⁾ is shown in Fig. 1.

The proposed smart temperature sensor is composed of only one voltage source. The sensor requires 19 MOSFETs.⁽⁷⁾ The power consumption is 195.15 nW. The conventional smart temperature sensor is composed of at least two power sources. The advantages of low power and small size make the sensor easily combine with other circuits that require temperature information.

2. Temperature Sensor

Figure 2 depicts the schematic of the temperature sensor. The sensor is based on a nine-transistor temperature-dependent current source. The output voltage of the threshold reference (M_1 – M_7) appears on nodes C and D and their values are described as follows:

$$V_C = V_T \left\{ 1 + \frac{\sqrt{\frac{W_1/L_1}{W_2/L_2}}}{\sqrt{\frac{W_1/L_1}{W_2/L_2} - \sqrt{\frac{W_5/L_5}{W_7/L_7} - \sqrt{\frac{W_5/L_5}{W_6/L_6}}}} \right\}, \quad (1)$$

$$V_D = V_T \left\{ 1 + \frac{\sqrt{\frac{W_5/L_5}{W_7/L_7}}}{\sqrt{\frac{W_1/L_1}{W_2/L_2} - \sqrt{\frac{W_5/L_5}{W_7/L_7} - \sqrt{\frac{W_5/L_5}{W_6/L_6}}}} \right\}, \quad (2)$$

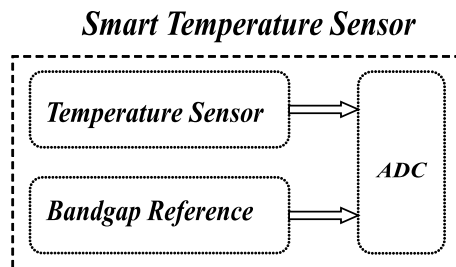


Fig. 1. Smart temperature sensor.

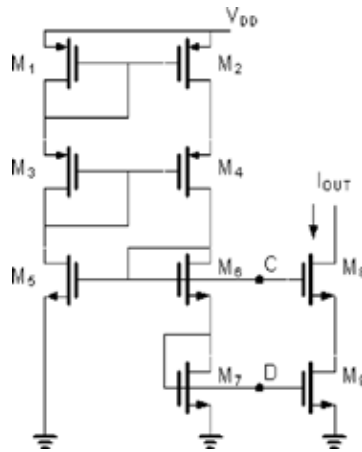


Fig. 2. Detailed circuit of the temperature sensor.

where V_T is the threshold voltage of MOSFET. W_i and L_i are the channel width and channel length of MOSFET M_i , as shown in Fig. 2, respectively. From eqs. (1) and (2), it is obvious that V_C and V_D , as shown in Fig. 2, depend only on the threshold voltage V_T and the geometric ratios of the transistor channel sizes. The voltage V_D controls the gate of the transistor M_9 , yielding the drain current I_{OUT} described as

$$I_{OUT} = \beta_9 (V_T)^2 \left\{ 1 + \frac{\sqrt{\frac{W_1/L_1}{W_2/L_2}}}{\sqrt{\frac{W_1/L_1}{W_2/L_2} - \sqrt{\frac{W_3/L_3}{W_7/L_7} - \sqrt{\frac{W_5/L_5}{W_6/L_6}}}}} \right\}^2 = (\text{const.}) \beta_9 (V_T)^2, \quad (3)$$

where β_9 is the gain factor of MOSFET M_9 . The temperature dependence of the output current can be expressed as

$$\frac{\Delta I_{OUT}}{I_{OUT}} = \left\{ \frac{1}{\beta} \frac{d\beta}{dT} + \frac{2}{V_T} \frac{dV_T}{dT} \right\} \Delta T. \quad (4)$$

The dependences of threshold voltage V_T and gain factor β are on the order of

$$\frac{\Delta I_{OUT}}{\Delta T} \approx -1.8 \text{ mV/K}, \quad (5)$$

$$\frac{1}{\beta} \frac{\Delta \beta}{\Delta T} \approx -0.5\%/K. \quad (6)$$

Therefore, the output current shows a temperature dependence of about 0.9 to 1% and can be regarded as a temperature signal. A current-to-frequency converter⁽⁸⁾ also converts the temperature signal to a digital signal. The temperature-dependent current I_{OUT} provides the current supply of a ring oscillator to generate a temperature-related square wave.

3. Ring Oscillator

In a simple ring oscillator, the total number of inverters must be odd, as shown in Fig. 3. For example, when the ring oscillator is implemented by a five-stage inverter chain, the ring oscillator can provide a frequency of $(1/10t_D)$.⁽⁹⁾

An oscillator is used as an element to convert signals from analog to digital. The relationship between output frequency and temperature is shown as follows:⁽¹⁰⁾

$$f_{out} = \frac{1}{2Nt_D} = \frac{\mu_n C_{OX}(V_{DD} - V_{TN})}{2N\left(\frac{L}{W}\right)_n C_L} \frac{1}{\ln\left(\frac{1.5V_{DD} - 2V_{TN}}{0.5V_{DD}}\right)}, \quad (7)$$

$$= \frac{\mu_0 \left(\frac{T}{T_0}\right)^{km} [V_{DD} - V_{TP} + |\alpha|(T - T_0)]}{2N\left(\frac{L}{W}\right)_n C_L} \frac{1}{\ln\left\{3 - \frac{[V_{TP} - |\alpha|(T - T_0)]}{0.5V_{DD}}\right\}}, \quad (8)$$

where $V_{TN} = V_{TP} = V_T$, i.e., the rising time (t_{PLH}) is assumed to be the same as the falling time (t_{PHL}).

4. Construction of Proposed Proportional-to-Absolute Temperature (PTAT)

The main requirements for the circuit design are low cost and low power consumption. As the intelligent network system becomes readily accessible, there is an increasing opportunity for incorporating the sensor chip into the system-on-chips.

The sensor chip has even been applied in the radio frequency identification (RFID). It is therefore important to consider the surface area and power consumption of a sensor chip. The circuits can be divided into two parts: a PTAT current-generating circuit and a current-driving ring oscillator. Figure 4 depicts the construction of the smart sensor. The PTAT subcircuits provide a supply current to drive the rear ring oscillator. The current generated by PTAT is microampere and is proportional to temperature. By doing so, the oscillator will generate an output wave that is absolutely temperature-related.

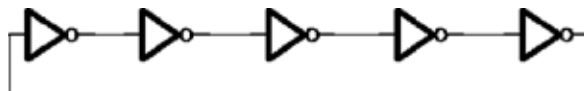


Fig. 3. Five-stage ring oscillator.

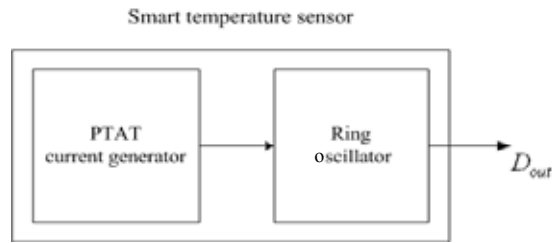


Fig. 4. Block diagram of the proposed temperature sensor.

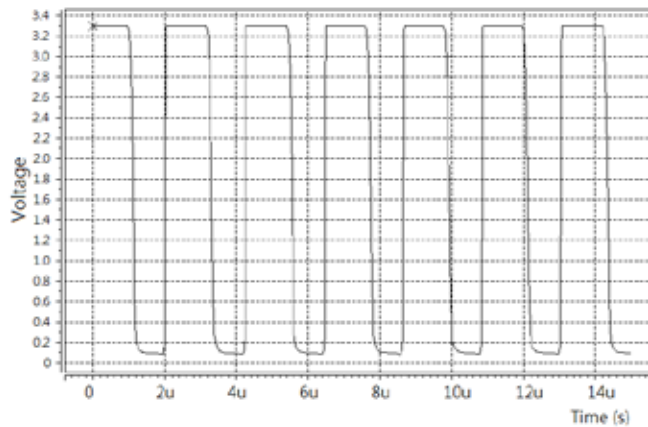


Fig. 5. Output waveform of the ring oscillator.

5. Simulation Results and Implementations

Figure 5 shows the output of this proposed circuit when the temperature is 25 °C in postsimulation. From the waveform results, we can observe that the sensor can still work properly. Figure 6 shows the relationship between the PTAT current and temperature. From Fig. 6, it is obvious that PTAT current has good linearity in the valid temperature range. An R -square value of 0.98 has been reached. Therefore, the simulation result can be acceptable.

The layout of the power source is separated into analog and digital regions because of noise reduction. The approach prevents the noise generated by the digital circuit from affecting the analog circuit. In addition, a guard ring is placed on the periphery of the analog circuit for resisting external noises.

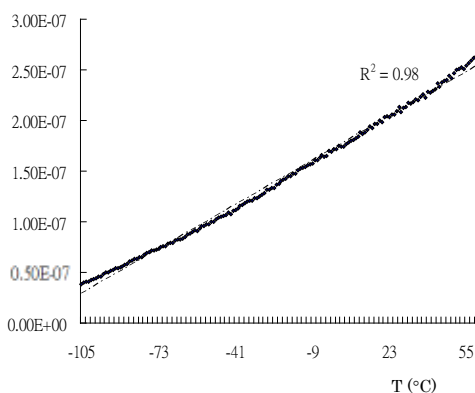


Fig. 6. Linearity of PTAT current vs temperature.

6. Conclusions

To reflect the demand of low cost and low power, a smart temperature sensor is successfully developed. The sensor circuit uses the PTAT connected to only one power supply. The ring oscillator generates the oscillated signal proportional to temperature. The approach is simpler than the convention one and the approach can reduce the complexity of this sensor.

The smart temperature sensor was fabricated by the 0.35 μm 2P4M TSMC CMOS process technology. The core area is only 1105.59 μm^2 . The temperature sensitivity range is about 165 $^{\circ}\text{C}$ and the power consumption is about 159.15 nW. It has an *R*-square value of 0.98. Therefore, a low-power and low-cost temperature sensor is realized.

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